

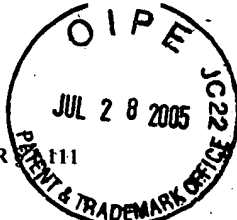
AMENDMENT AND RESPONSE UNDER 37 CFR 1.111

Serial Number: 09/941,068

Filing Date: August 28, 2001

Title: CO-SIMULATION OF VERILOG/PLI AND SYSTEM C MODULES USING REMOTE PROCEDURE CALL

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S/N 09/941,068

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Qiyong Bian

Examiner: Thai Q. Phan

Serial No.: 09/941,068

Group Art Unit: 2128

Filed: August 28, 2001

Docket No.: 303.741US1

Title: CO-SIMULATION OF VERILOG/PLI AND SYSTEM C MODULES USING
REMOTE PROCEDURE CALL

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This responds to the Office Action mailed on January 26, 2005. Please amend the above-identified patent application as follows.

This response is accompanied by a Petition, as well as the appropriate fee, to obtain a three-month extension of the period for responding to the Office Action, thereby moving the deadline for response from April 26, 2005 to July 26, 2005.